

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Paul A. Martin et al.

Assignee: Sun Microsystems, Inc.

Title: Concurrent Shared Object Implemented Using A Linked List With Amortized Node Allocation

Patent No.: 7,017,160 Issued: March 21, 2006

Atty. Docket No.: 004-4665-1

July 26, 2006

ATTN: Certificate of Correction Branch
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT –
PTO MISTAKE (37 C.F.R. § 1.322(a))**

Dear Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322(a), please issue a Certificate of Correction in the above-identified matter. The mistake(s) to be corrected was made by the Office.

1. Attached hereto is Form PTO/SB/44 (PTO-1050) suitable for printing.
2. The exact page(s) and line number(s) where the errors occur in the “Other Publications” section of the issued patent are shown correctly in the Form PTO-1449 as filed on February 7, 2002.

Page 2 of 2, line AK.

3. The exact page(s) and line number(s) where the errors occur in the specification are shown correctly in the Examiner’s Amendment received with the Notice of Allowance mailed April 15, 2005:

Page 1, paragraph (1)

4. The exact page(s) and line number(s) where the errors occur in the specification are shown correctly in the patent application as originally filed:

Page 2, paragraph [1006], line 6.

Page 3, paragraph [1010], line 6.

Page 11, paragraph [1041], line 14.

Page 12, paragraph [1044], line 7.

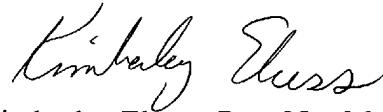
Page 12, paragraph [1046], line 4.

Page 13, paragraph [1048], line 5.
Page 20, paragraph [1066], line 7.
Page 21, paragraph [1069], line 18.
Page 23, paragraph [1076], line 9.
Page 23, paragraph [1076], line 19.
Page 26, paragraph [1085], line 10.
Page 26, paragraph [1086], line 3.
Page 26, paragraph [1086], line 5.
Page 32, paragraph [1101], line 13.
Page 39, paragraph [1119], line 4 (numbered line 17).
Page 39, paragraph [1119], line 5 (numbered line 18).
Page 48, claim 32, line 1.

5. Please send the Certificate to:

Kimberley Elcess
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7600B N. CAPITAL OF TEXAS HWY, SUITE 350
AUSTIN, TEXAS 78731-1191

Respectfully submitted,



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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 7,017,160

DATED : March 21, 2006

INVENTOR(S) : Martin et al.

It is certified that error(s) appear(s) in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

"Other Publications" section, page 2, column 2, 8th listed reference, delete "M.P. Herlighy" and replace with --M.P. Herlihy--;

Column 1, line 11, delete "abandoned" and replace with --pending--;

Column 1, line 48, delete "lock-released" and replace with --lock released--;

Column 2, line 25, delete "AU" and replace with --All--;

Column 7, line 23, delete "*addr2 = new2;" and replace with ---*addr2 = new2;;;

Column 8, line 2, delete "fill" and replace with --full--;

Column 8, line 21, delete "fill" and replace with --full--;

Column 8, line 46, delete "pop_right (changes" and replace with --pop_right () changes--;

Column 12, line 61, delete "right When" and replace with --right. When--;

Column 13, line 57, delete "deque," and replace with --deque.--;

Column 15, line 12, delete "Node allocate_right_nodes9int howMany) {" and replace with --Node allocate_right_nodes (int howMany) {--;

Column 15, line 20, delete "lastNode = newNode" and replace with --lastNode = newNode;--;

Column 17, line 4, delete "RaN" and replace with --RN--;

Column 17, line 11, delete "possibly" and replace with --(possibly--;

Column 17, line 14, delete "retrying" and replace with --retrying--;

MAILING ADDRESS OF SENDER:

ZAGORIN O'BRIEN GRAHAM LLP

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Austin, Texas 78731

PATENT NO. 7,017,160

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 7,017,160

DATED : March 21, 2006

INVENTOR(S) : Martin et al.

It is certified that error(s) appear(s) in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 20, line 47, delete "if (v = DCAS(&chop->V,&rptr->V,RN,RN,RX,RY)) {" and replace with -- if (v = DCAS(&chop->V,&rptr->V,RN,RN,RX,RY)) {--;

Column 24, line 63, delete "LFRCStoreAlloc(&LeftHat->R,RightHat);" and replace with -- LFRCStore(&LeftHat->R,RightHat);--;

Column 24, line 64, delete " LFRCStoreAlloc(&RightHat->L,LeftHat);" and replace with -- LFRCStore(&RightHat->L,LeftHat);--;

Column 30, claim 32, line 1, add --(deque)-- after "queue".

MAILING ADDRESS OF SENDER:

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PATENT NO. 7,017,160